



Our Docket No: 42P9978C

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

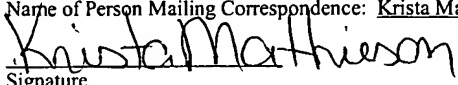
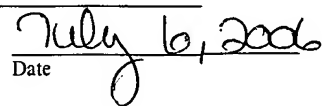
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|--|---|----------------------------|
| In re Application of |) | |
| |) | |
| Maeda, et al. |) | Group Art Unit: 2822 |
| |) | |
| Serial No.: 10/762,013 |) | Examiner: Novacek, Christy |
| |) | |
| Filed: January 21, 2004 |) | |
| |) | |
| For: Flip Chip Interconnection Using No- Clean Flux |) | |
| |) | |

PRELIMINARY AMENDEMENT

Mail Stop: RCE
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination of the above-referenced application, please enter the following amendments and consider the following remarks.

| | |
|---|--|
| FIRST CLASS CERTIFICATE OF MAILING | |
| I hereby certify that I am causing the above-referenced correspondence to be deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and that this paper or fee has been addressed to the Commissioner for Patents, Alexandria, VA 22313. | |
| Date of Deposit: | July 6, 2006 |
| Name of Person Mailing Correspondence: | Krista Mathieson |
|  Signature |  Date |